

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	INTEGRATED CIRCUIT DEVICE PACKAGE HAVING A SUPPORT COATING FOR IMPROVED RELIABILITY DURING TEMPERATURE CYCLING							
Application Number : Date : First Named Applicant: Viraj A. Patwardhan Confirmation Number: Attorney Docket Number: NSC1P131X3								
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Justin A. White Registered Number: 48,833</td><td>/JAW/</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Justin A. White Registered Number: 48,833	/JAW/	Attorney
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Documents being submitted:	Files
us-fee-sheet	NSC1P131X3submission-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-assignment	NSC1P131X3submission-usassn.xml us-assignment.xsl us-assignment.dtd assign1.tif assign2.tif assign3.tif
us-request	NSC1P131X3submission-usrequ.xml us-request.dtd us-request.xsl
us-declaration	DEC1.tif
us-declaration	DEC2.tif
us-declaration	DEC3.tif
application-body	NSC1P131X3-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fig1a1b.tif fig2abc.tif fig34a4b.tif fig5abc.tif fig67a7b.tif
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